FIGURE 1 - Standard Network Processor Configuration (Prior Art)
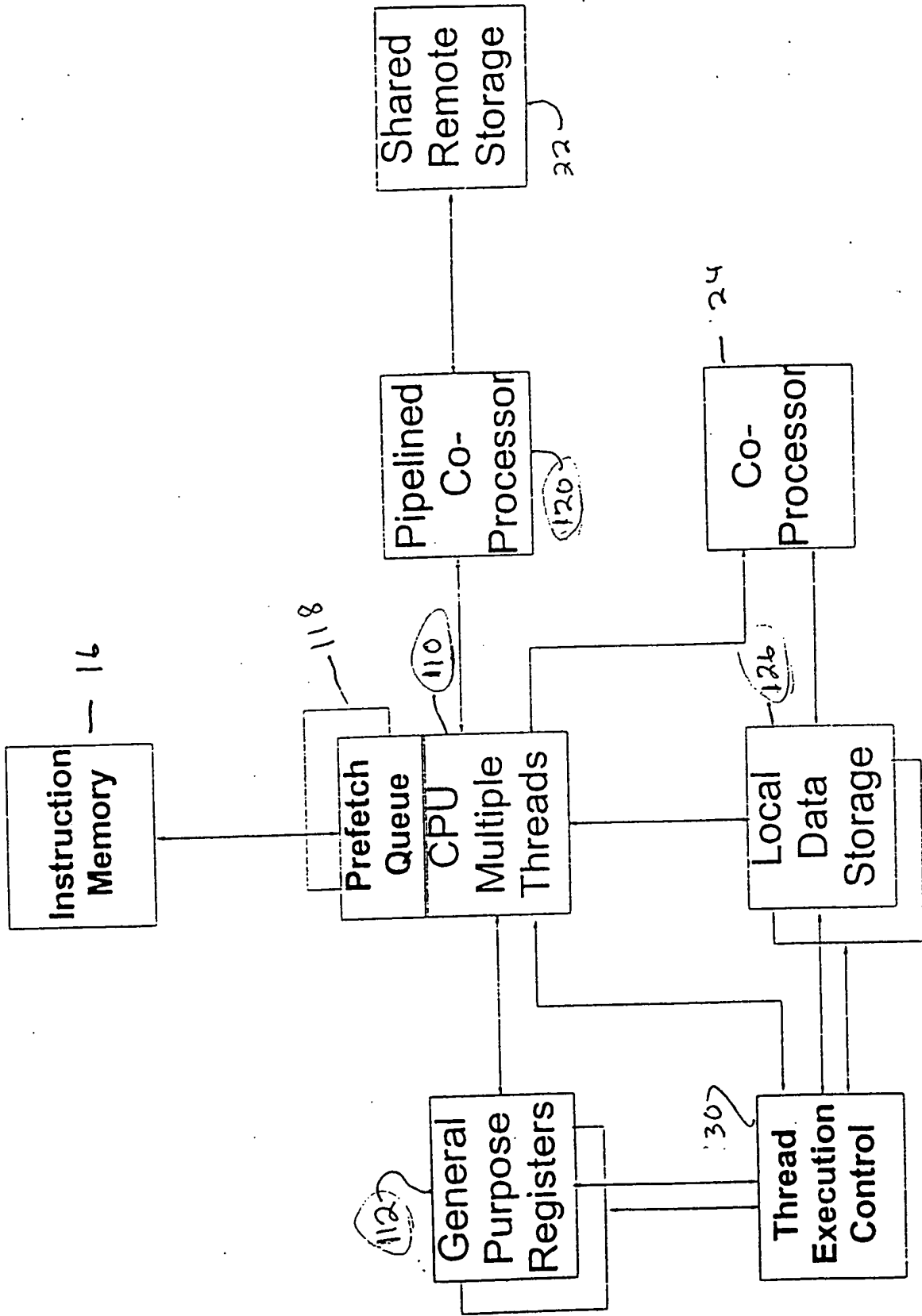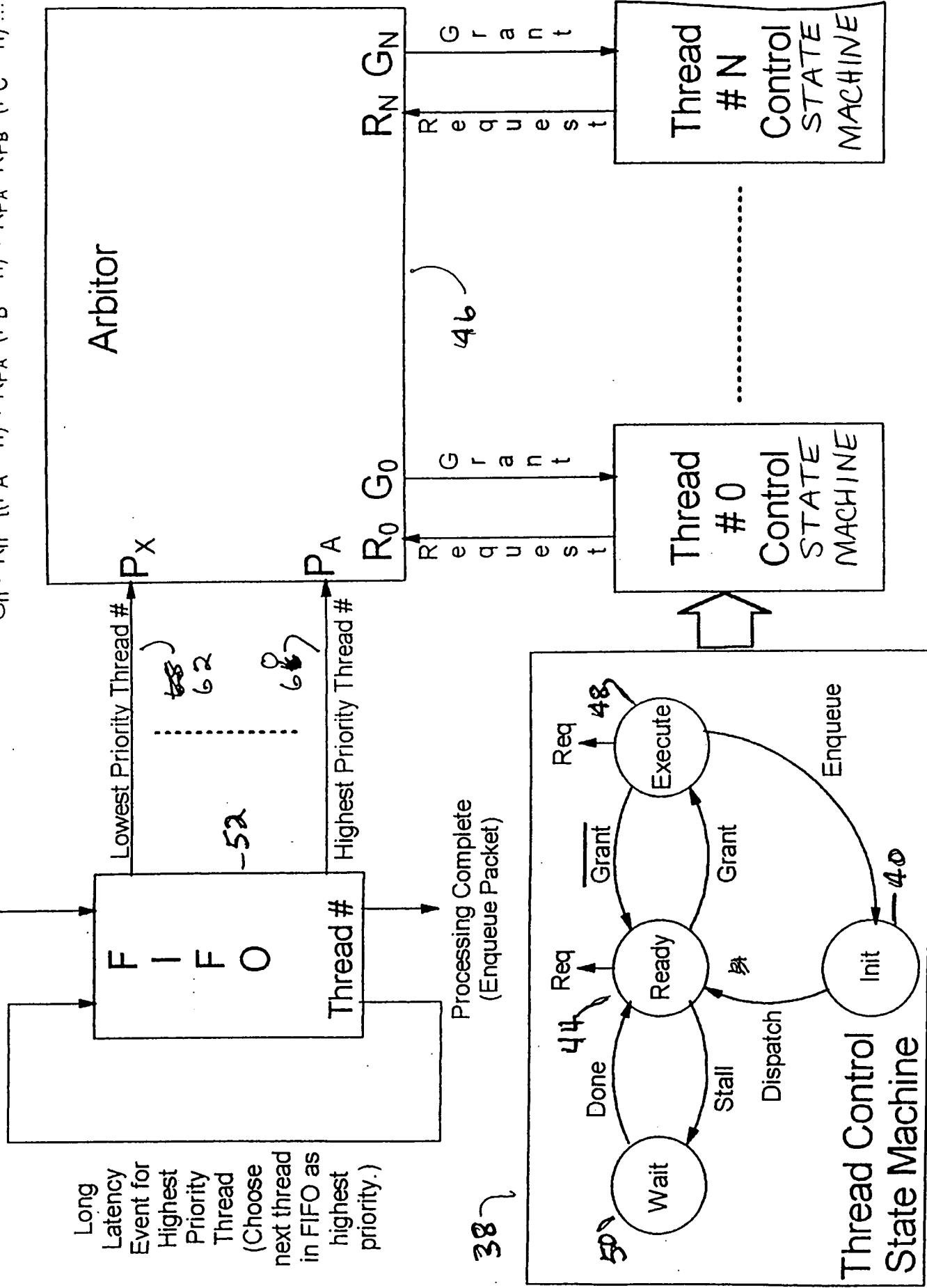
FIGURE 2 - Network Processor Configuration with Multiple Execution Threads

FIGURE 3 - Thread Execution Control

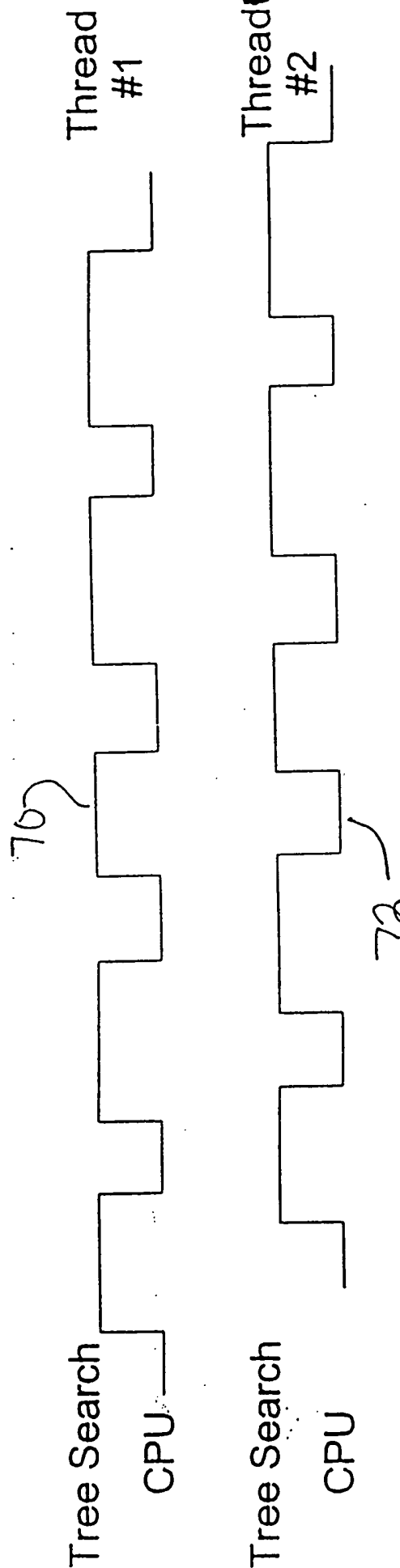$G_n = R_n \cdot \{(P_A = n) + \overline{R_{PA}} \cdot (P_B = n) + R_{PA} \cdot \overline{R_{PB}} \cdot (P_C = n)\} \ldots$

FIGURE 4 - Multiple Execution Threads